

FIG. 1

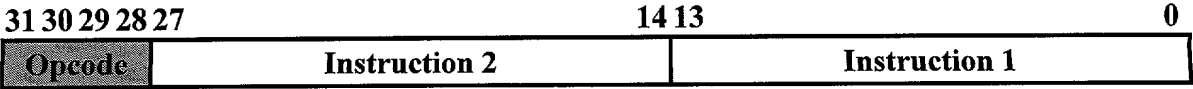


Fig. 2

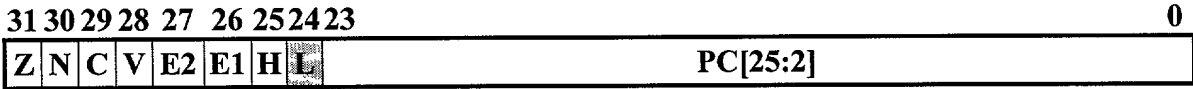


Fig. 3

13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	0	0	cc			8 bit signed offset							

Fig. 4

13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	0	1	a			b			op				

Fig. 5

13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	0	1	a			subop			op=31				

Fig. 6

13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	0	1	Imp_op			subop=7			op=31				

Fig. 7

13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	1	0	A			b			op	4 bit offset			

Fig. 8

3	12	11	10	9	8	7	6	5	4	3	2	1	0
0	1	1	A			b			op	4 bit offset			

Fig. 9

13	12	11	10	9	8	7	6	5	4	3	2	1	0
1	0	0	A			op	7 bit offset						

Fig. 10

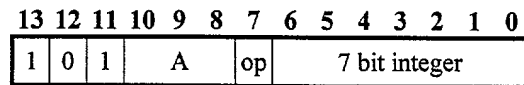


Fig. 11

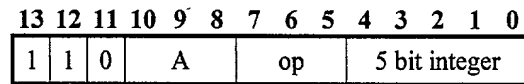


Fig. 12

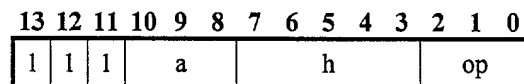


Fig. 13

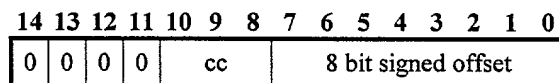


Fig. 14

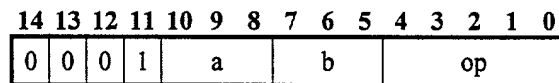


Fig. 15

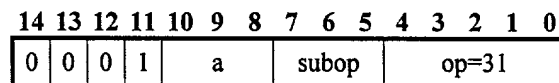


Fig. 16

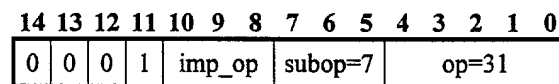


Fig. 17

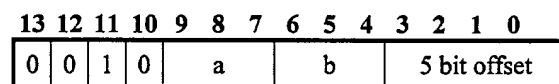


Fig. 18

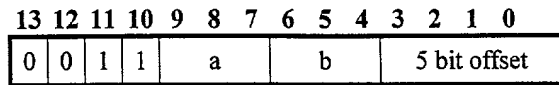


Fig. 19

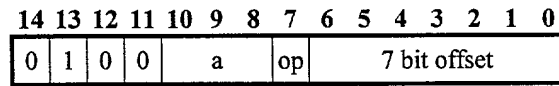


Fig. 20

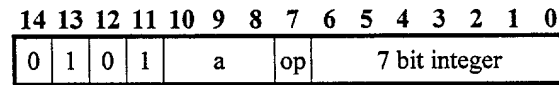


Fig. 21

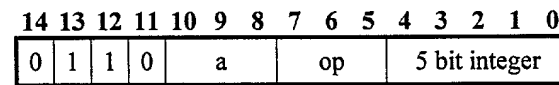


Fig. 22

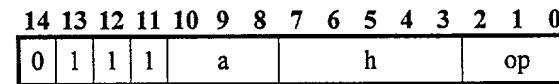


Fig. 23

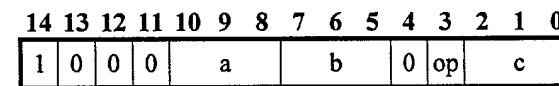


Fig. 24

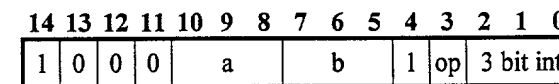


Fig. 25

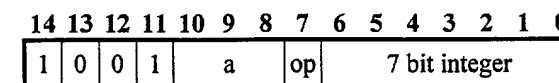


Fig. 26

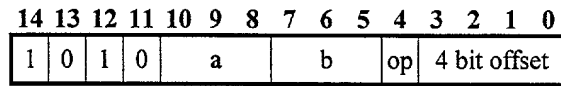


Fig. 27

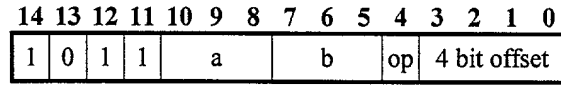


Fig. 28

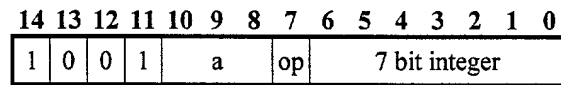


Fig. 29

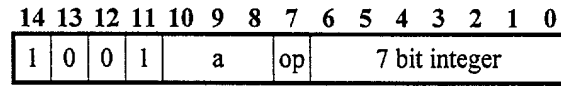


Fig. 30

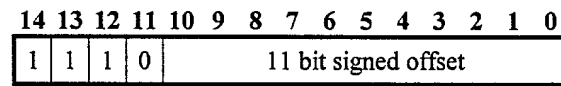


Fig. 31

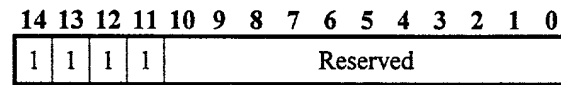
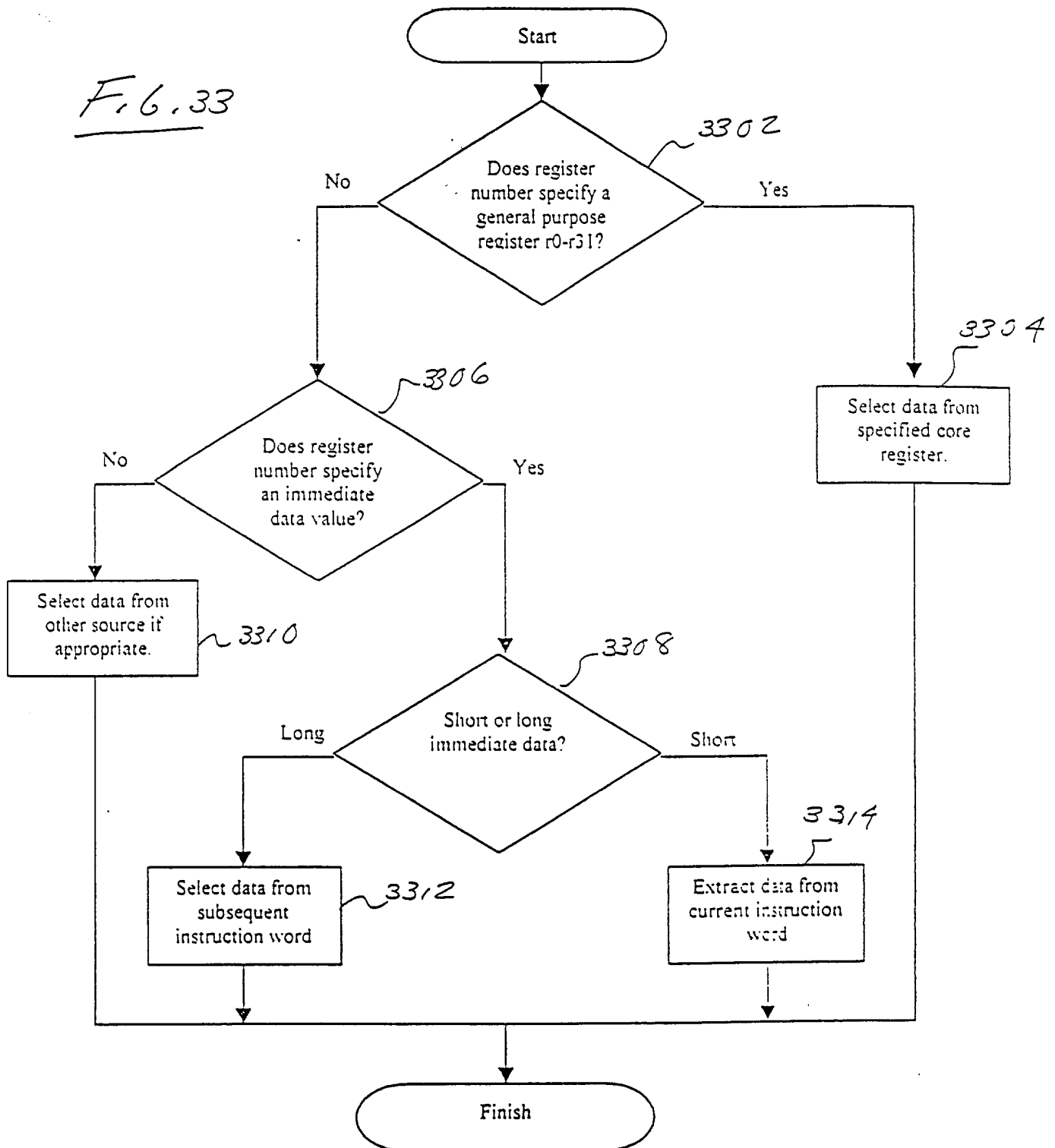


Fig. 32

F.6.33



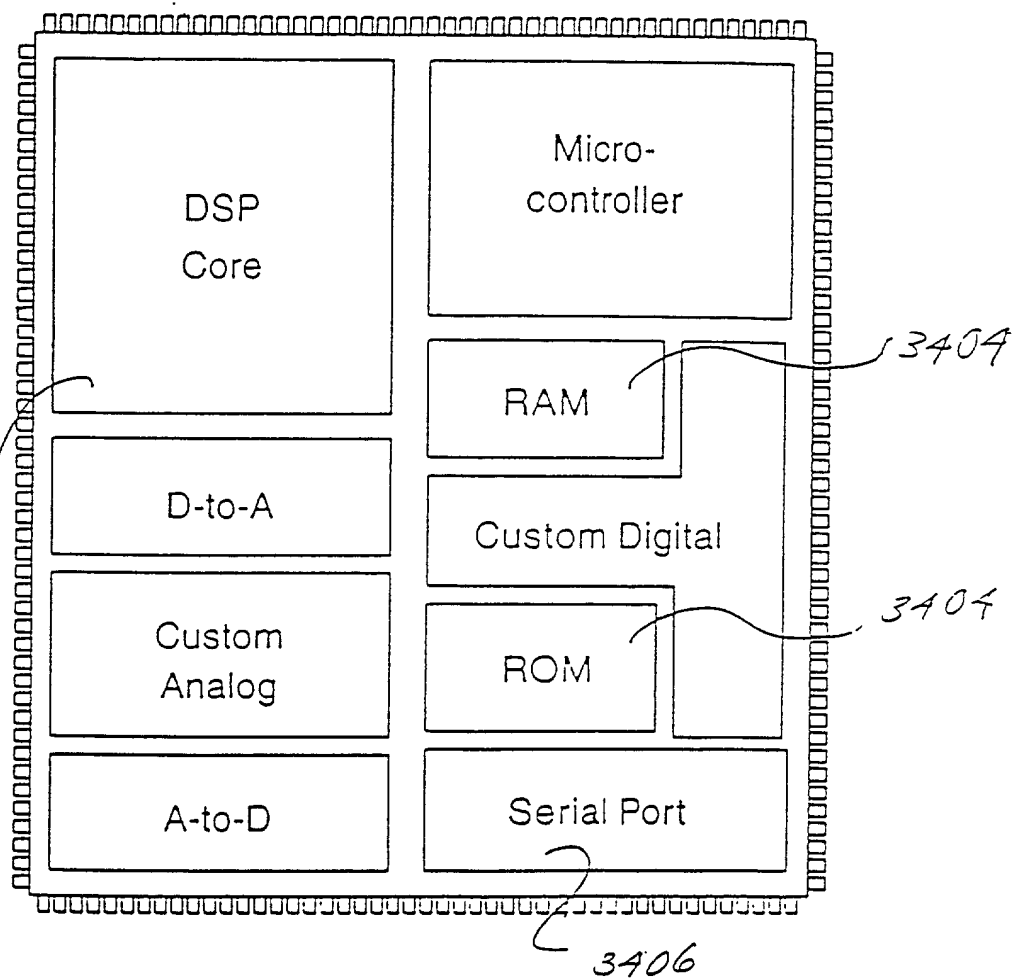


FIG. 34

FIG. 35

